LCC1413-A - July 28, 2023

Item # LCC1413-A was discontinued on July 28, 2023. For informational purposes, this is a copy of the website content at that time and is valid only for the stated product.

FULL-WAVE LIQUID CRYSTAL VARIABLE RETARDERS / WAVE PLATES

LCC1423-A 0 nm to >λ Retardance. Ø20 mm Clear Aperture

- ► Nematic Liquid Crystal Full-Wave Variable Retarders
- Available with Ø10 mm or Ø20 mm Clear Aperture
- AR Coated for Visible, NIR, or MIR Light
- ► Compensated Models Achieve 0 nm Retardance



LCC1223-B ~30 nm to >λ Retardance Ø20 mm Clear Aperture



LCC1413-C 0 nm to >λ Retardance, Ø10 mm Clear Aperture



K-Cube™ LC Controlle



LCC25 Benchtop LC Controller

Hide Overview

OVERVIEW

- Variable Wave Plate to Actively Control the Polarization State and/or Phase Delay of Light
- Retardance Range
 - $\bullet~$ 0 nm to λ for LC Retarders with Residual Retardance Compensation
 - ~30 nm to λ for Uncompensated LC Retarders
- · Clear Aperture: Ø10 mm or Ø20 mm
- · Low Retardance Uniformity (See Specs Tab for Details)
- Compatible with Our LCC25 and KLC101 Voltage Controllers (Sold Separately Below)

Thorlabs' Full-Wave Liquid Crystal Variable Retarders (LCVR) use a nematic liquid crystal cell to function as a variable wave plate. The absence of moving parts provides quick switching times on the order of milliseconds (see the Switching Time tab for details). AR coatings are available for five wavelength ranges: 350 - 700 nm, 650 - 1050 nm, 1050 - 1700 nm. 1650 - 3000 nm. or 3600 - 5600 nm (see the Performance tab for transmission and retardance data).

Thorlabs offers Ø10 mm and Ø20 mm clear aperture sizes. Both sizes are available in one of two variable retardation ranges: 0 nm to λ retardation for retarders with residual retardance compensation, and ~30 nm to λ retardation for uncompensated LC retarders. Our compensated retarders integrate a phase compensator made of liquid crystal

polymers, which compensate the residual retardation of the LCVR to achieve true zero retardation at a specific driving voltage (see the Performance tab for more information). Its structure and principle are described below.

Our Ø10 mm feature a 1" outer diameter, making them compatible with our Ø1" optics mounts. Our Ø20 mm retarders have a 2" outer diameter, providing compatibility with our Ø2" optics mounts. Please refer to the Specs tab for suggested compatible mounts.

Performance

These liquid crystal variable retarders provide excellent uniformity, low optical losses, and low wavefront distortion. Our retarders also provide a quick switching time, a broad operating temperature range, and a wide wavelength range. Please see the Specs, Performance, and Switching Time tabs for complete details. We also offer temperaturestabilized full-wave retarders for increased long-term stability.

Operating Principle





Low Retardance

Figure 1: In their nematic phase, liquid crystal molecules have an ordered orientation, which together with the stretched shape of the molecules creates an optical anisotropy. When an electric field is applied, the molecules align to the field and the level of birefringence is controlled by the tilting of the LC molecules

Compensating for Residual Retardance



Click to Enlarge



Compensated LC Retarder

Figure 2: The minimum retardation of uncompensated liquid crystal retarders is ~30 nm. Our compensated LC retarders include a liquid crystal polymer compensator with a fixed retardation and slow axis that is orthogonal to that of the variable LC cell. This enables our compensated retarders to achieve a minimum retardance of 0 nm

Selection Guide for LC Retarders						
Туре	Clear Aperture					
Half Wave	Ø10 mm or Ø20 mm					
Half Wave, Thermally Stabilized	Ø10 mm					
Full Wave	Ø10 mm or Ø20 mm					
Full Wave, Thermally Stabilized	Ø20 mm					
Multi-Wave	Ø10 mm					
Multi-Wave, Integrated Controller	Ø10 mm					
Custom LC Retard	ders					

Operation

As illustrated in Figure 1, a Liquid Crystal Variable Retarder consists of a transparent cell filled with a solution of Liquid Crystal (LC) molecules and functions as a variable wave plate. The orientation of the LC molecules is determined by the alignment layer in the absence of an applied voltage. The alignment layer is composed of an organic polyimide (PI) coating whose molecules are aligned in the rubbing direction during manufacturing. Due to the birefringence of the LC material, this LC retarder acts as an optically anisotropic wave plate, with its slow axis, marked on the mechanical housing, parallel to the surface of the retarder. Two parallel inner faces of the cell wall are coated with a transparent conductive film so that a voltage can be applied across the cell. When an AC voltage is applied, the LC molecules will reorient from their default alignment according to the applied V_{rms}. Hence, a varying applied voltage can actively control the retardance of the liquid crystal variable retarder

Residual Retardation Compensation

Due to the surface anchoring of the PI layer, even when a voltage is being applied, there are still some liquid crystal molecules that cannot be reoriented, especially near the alignment layers. This results in the residual retardation of the LC retarder during operation. Thorlabs' uncompensated LC retarders have ~30 nm residual retardation at 25 V_{rms} driving voltage, as depicted in Figure 2 above. To meet the need for true zero retardation in sensitive applications, we offer the compensated LC retarders. A compensating layer made of liquid crystal polymer (LCP) is bonded on the LC cell, with its slow axis perpendicular to that of the LC cell. The fixed retardation of LCP compensator is ~50 nm. Thus, at a specific driving voltage between 5 V and 20 V, the retardation of the LC cell and the compensator cancel each other, resulting in true zero retardation. This comes at a cost of slightly degraded retardation uniformity and higher overall thickness. See the Specs and Performance tabs for more information.

Housing Thickness

The LCC25 and KLC101 liquid crystal controllers provide active DC offset compensation while applying an AC voltage (0 to 25 V_{rms}). The DC offset compensation automatically zeros the DC bias across the LC device in order to counteract the buildup of charge.

Hide Specs

SPECS										
Compensated Full-Wave LC Retarders										
Item #		LCC1413-A	LCC1423-A	LCC1413-B	LCC1423-B	LCC1413-C	LCC1423-C			
Wavelength Range		350 -	700 nm ^a	650 -	1050 nm	1050 -	1700 nm			
Retardance Range				0 nr	m to >λ					
Clear Aperture		Ø10 mm	Ø20 mm	Ø10 mm	Ø20 mm	Ø10 mm	Ø20 mm			
Housing Outer Dim	nensions	Ø1" ^b (Ø25.4 mm)	Ø2.20" ^c (Ø55.8 mm)	Ø1" ^b (Ø25.4 mm)	Ø2.20" ^c (Ø55.8 mm)	Ø1" ^b (Ø25.4 mm)	Ø2.20" ^c (Ø55.8 mm)			
Liquid Crystal Mate	I Material Nematic Liquid Crystal									
Surface Quality		60-40 Scratch-Dig								
Parallelism		<5 arcmin								
Switching Time (Rise/Fall, Typical)	d	39.4 ms / 310 μs @ 25.6 °C 129 ms / 660 μs @ 25.6 °C 209 ms / 193 μs @ 25.6					3 µs @ 25.6 °C			
Damage	Pulsed (ns)		J/cm ² z, 8 ns, Ø200 μm)	3.56 J/cm ² (810 nm, 10 Hz, 7.6 ns, Ø234 μm)			J/cm ² z, 10 ns, Ø458 μm)			
Threshold	Pulsed (fs)	1	4 J/cm ² z, 76 fs, Ø162 μm)		3 J/cm ² , 36.4 fs, Ø189 μm)	1	0.161 J/cm ² (1550 nm, 100 Hz, 70 fs, Ø145 μm)			
AR Coating		R _{avg} < 0.5% ^e								
Wavefront Distortion	on	≤N/4 (@ 635 nm)								
Retardance Uniform	nity (RMS) ^f			<	λ/20					
Housing Thickness	3	8.6 mm (0.34")	15.0 mm (0.59")	8.6 mm (0.34")	15.0 mm (0.59")	8.6 mm (0.34")	15.0 mm (0.59")			
Storage Temperatu	ire			-30 t	o 70 °C					

Operation Temperature Uncompensated Full-Wave LC Retarders Item # LCC1113-A LCC1223-A LCC1113-B LCC1223-B LCC1113-C LCC1223-C LCC1113-D LCC1113-MIR Wavelength Range 350 - 700 nm^a 650 - 1050 nm 1050 - 1700 nm 1650 - 3000 nm 3600 - 5600 nm Retardance Range ~30 nm to >λ Clear Aperture Ø10 mm Ø20 mm Ø10 mm Ø20 mm Ø20 mm Ø10 mm Ø10 mm Ø10 mm Housing Ø1"b Ø1"b Ø1"b Ø1"b Ø1"b Ø2"g Ø2"g Ø2"g **Outer Dimensions** (Ø25.4 mm) (Ø50.8 mm) (Ø25.4 mm) (Ø50.8 mm) (Ø25.4 mm) (Ø50.8 mm) (Ø25.4 mm) (Ø25.4 mm) Liquid Crystal Material Nematic Liquid Crystal 40-20 Scratch-Dig 60-40 Scratch-Dig Surface Quality Parallelism <5 arcmin Switching Time 59.6 ms / 1.45 ms 124 ms / 2.94 ms 236 ms / 4.89 ms 586 ms / 7.0 ms 4.4 s / 94 ms @ 22 °C @ 25.6 °C @ 25.6 °C @ 25.6 °C @ 25.6 °C (Rise/Fall, Typical)d 0.082 J/cm² 2.04 J/cm² 3.56 J/cm² 2.5 J/cm² Pulsed (ns) N/A (2000 nm. 10 Hz. (532 nm, 10 Hz, 8 ns, Ø200 μm) (810 nm, 10 Hz, 7.6 ns, Ø234 μm) (1542 nm, 10 Hz, 10 ns, Ø458 μ m) 6.5 ns, Ø292 µm) Damage Threshold 0.05 J/cm² 0.024 J/cm² 0.023 J/cm² 0.161 J/cm² Pulsed (fs) (2000 nm, 100 Hz, (532 nm, 100 Hz, 76 fs, Ø162 μm) (800 nm, 100 Hz, 36.4 fs, Ø189 μ m) (1550 nm, 100 Hz, 70 fs, Ø145 μ m) 100 fs. Ø220 um) R_{avg} < 1.0%^e $R_{avg} < 0.5\%^e$ AR Coating ≤λ/4 (@ 635 nm) Wavefront Distortion <λ/50 <λ/10 Retardance Uniformity (RMS)f

-20 to 45 °C

8.0 mm (0.32")

Storage Temperature	-30 to 70 °C
Operation Temperature	-20 to 45 °C

- Liquid crystal is more susceptible to damage when exposed to light sources close to UV wavelengths. Our tests show that our liquid crystal variable retarders deteriorate when exposed to a 395 nm, 6 W/cm² light source for four hours. Using a 365 nm, 40 mW/cm² light source, damage occurs within 15 minutes. We recommend using this product with light sources having wavelengths of 400 nm or longer. For use with shorter wavelengths, power should be kept low and exposure duration limited. The shorter the wavelength, the more susceptible the liquid crystal is to damage.
- b. Recommended Mounts: RSP1(RSP1/M), CRM1T (CRM1T/M), CRM1PT (CRM1PT/M), KM100a c. Recommended Mounts: RSP2 (RSP2/M), LCRM2 (LCRM2/M), KM200T, SM2TC
- d. Switching speed is highly dependent on several factors, including voltage change and cell temperature. See the Switching Time tab for more details.
- e. At all air-to-glass surfaces for the specified wavelength range.
 - Specified Over the Entire Clear Aperture
 - Recommended Mounts: RSP2 (RSP2/M), LCRM2 (LCRM2/M), KM200

Hide Performance

f.

g.

PERFORMANCE

LC Retarder Performance

In their nematic phase, liquid crystals molecules have an ordered orientation, which together with the stretched shape of the molecules, creates an optical anisotropy. When an electric field is applied, the molecules align to the field and the level of effective retardance is controlled by the tilting of the LC molecules. To minimize effects due to ions in the material, an LC device must be driven using an alternating voltage. Our LCC25 and KLC101 controllers, sold below, are designed to minimize the DC bias in the driving signal in the operating range of 0 V to 25 V.

Due to changes in the molecular polarizability, the LC material exhibits higher chromatic dispersion at short wavelengths and comparably small chromatic dispersion at long wavelengths. To account for this. we provide the

Variable Full-Wave Retarder Performance Graphs							
Item #a	Wavelength Range	Retardance @ 25 °C	Retardance vs. Temperature	Temperature Stability	Transmission		
LCC1413-A			(635 nm)				
LCC1423-A	350 - 700 nm		(635 nm)	<u> </u>			
LCC1113-A			(635 nm)	_			
LCC1223-A			(635 nm)	-			
LCC1413-B			(780 nm)				
LCC1423-B	650 - 1050 nm		(780 nm)	<u> </u>			
LCC1113-B		N	N	_			
LCC1223-B			(780 nm)				
LCC1413-C		N	B				
LCC1423-C	1050 - 1700 nm		(1550 nm)	—			
LCC1113-C	1050 - 1700 1111	N	B				
LCC1223-C			(1550 nm)	-			
LCC1113-D	1650 - 3000 nm	K	(2200 nm)		/		
LCC1113-MIR	3600 - 5600 nm	Ы	(4400 nm)				

a. Our retarders with residual retardance compensation (represented by the green shaded rows) feature an LCP compensator plate. This enables our compensated retarders to achieve 0 nm retardance. Please refer to the *Overview* tab for more information.

retardance data at one or two select wavelengths within the product's wavelength range in the table to the right.

Additionally, the LC retardation also depends on the temperature of the device. As temperature increases, the retardation decreases with it. However, as seen in the Switching Time tab, the switching speed of the LC improves at higher temperatures. Generally, the LC's refractive indices (both ordinary and extraordinary) change more drastically as temperature nears the LC's clearing temperature. As such, we choose to use materials with a high clearing temperature to minimize the temperature dependence when used at room temperature.

Click Here to Download Retardance Data Click Here to Download Transmission Data

Temperature Stability

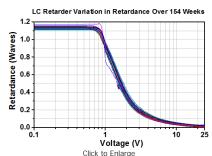
Our liquid crystal retarders have a retardance that slightly decreases with increasing temperature. The graphs in the table above compare the retardance at set drive voltages versus temperature. For temperature unregulated environments, Thorlabs' temperature controlled half-wave LCRs are suggested.

Click Here to Download Temperature Stability Data

Long-Term Stability

Our liquid crystal retarders exhibit consistent performance over time. The graph to the right shows the retardance vs. voltage for one previous-generation LCC1112-A three-quarter wave retarder, driven by our LCC25 liquid crystal controller over 154 weeks. The retardance was tested once per week and varied only slightly over the testing period. For the complete set of data from testing each week, please click below to download the full data file.

The graph below to the left shows that the retardance varies only slightly at a constant voltage, while the graph below to the right shows that the voltage varies only slightly at a constant retardance. Similar consistency in performance can also be expected for our other models of retarders. To maximize the long-term stability of our retarders, we recommend always using our LCC25 or KLC101 controllers. They are designed to reduce the DC voltage offset, thus minimizing charge buildup and maximizing stability.



Graph shows variation in retardance over a period of 154 weeks.

Click Here to Download Long-Term Performance Data





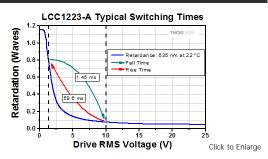
Hide Switching Time

SWITCHING TIME

LC Retarders Switching Time

Liquid crystal retarders feature a short switching time compared to mechanical variable wave plates due to the lack of moving parts. The switching time of a liquid crystal retarder depends on several variables, some of which are controlled in the manufacturing process, and some by the user.

In general, liquid crystal retarders will always switch faster at higher driving voltage than at lower driving voltage. Also, the fall time (from low voltage to high voltage) is faster than rise time (from high voltage to low voltage) when the LC retarder is switched between two voltages. The graph to the right depicts examples of switching between 1.35 V and 10 V. If faster switching speeds are desired, we recommend using the retarder together with a fixed wave plate so that the retarder can be used at a higher voltage.



In addition, the material's viscosity and hence the switching speed also depend on temperature of the LC material. As can be seen below, the switching speed can increase by as much as two times by heating the LC retarder. Our standard LC retarders are designed to work at temperatures up to 50 °C, where they can still maintain the specified retardation. If additional speed is required, the retarders can work at temperatures up to 70 °C, but the maximum retardation value will be lower

The switching speed is also related to the thickness of the LC retarder, the rotational viscosity of the LC material, and the dielectric anisotropy of the LC material. However, since each of those variables affects other operating parameters as well, our LC retarders are designed to optimize overall performance, with a special emphasis on switching time. We also offer custom and OEM LC retarders optimized for other parameters, as well as faster liquid crystal retarders. See the *Custom Capabilities* tab above, or contact Tech Support for details.

Sample Switching Times at Various Temperatures

Switching times were tested by measuring the rise time from V_1 to V_2 and the fall time from V_2 to V_1 with the liquid crystal retarder being held at the specified temperature. V_1 is fixed at 10 V for all the tests, and V_2 is the voltage at which the retardation is the maximum specified value for the retarder (1 λ). Please note that switching times at lower voltages (for instance, if $V_1 = 5$ V) are longer than the switching times specified below.

Compensated Retarders

LCC1413-A, LCC1423-A (AR Coating: 350 - 700 nm)							
Temperature V ₁ V ₂ Rise Time Fall Time							
25.6 °C	10	1.01	39.4 ms	310 µs			
45 °C 10 1.01 27.6 ms 211 μs							
60 °C	10	1.01	16.5 ms	146 µs			
70 °C	10	1.01	14.0 ms	126 µs			

LCC1413-B, LCC1423-B (AR Coating: 650 - 1050 nm)						
Temperature	V ₁	V ₂	Rise Time	Fall Time		
25.6 °C	10	1.09	129 ms	660 µs		
45 °C	10	1.09	85.8 ms	434 µs		
60 °C	10	1.09	51.3 ms	209 µs		
70 °C	10	1.09	41.3 ms	189 µs		

LCC1413-C, LCC1423-C (AR Coating: 1050 - 1700 nm)						
Temperature	V ₁	V ₂	Rise Time	Fall Time		
25.6 °C	10	0.86	209 ms	193 µs		
45 °C	10	0.86	113 ms	140 µs		
60 °C	10	0.86	83.6 ms	76.9 µs		
70 °C	10	0.86	55.2 ms	74.5 µs		

Uncompensated Retarders

LCC1113-A, LCC1223-A (AR Coating: 350 - 700 nm)					
Temperature V ₁ V ₂ Rise Time Fall Time					
22 °C	10	1.35	59.6 ms	1.45 ms	
45 °C	10	1.35	41.5 ms	990 µs	
60 °C	10	1.35	30.5 ms	685 µs	
70 °C	10	1.35	26.3 ms	591 µs	

LCC1113-B, LCC1223-B (AR Coating: 650 - 1050 nm)						
Temperature V ₁ V ₂ Rise Time Fall Time						
25.6 °C	10	1.6	124 ms	2.94 ms		
45 °C	10	1.6	82.6 ms	1.93 ms		
60 °C	10	1.6	65.2 ms	934 µs		
70 °C	10	1.6	52.7 ms	843 µs		

LCC1113-C, LCC1223-C (AR Coating: 1050 - 1700 nm)						
Temperature V ₁ V ₂ Rise Time Fall Time						
25.6 °C	10	1.7	236 ms	4.89 ms		
45 °C	10	1.7	127 ms	3.54 ms		
60 °C	10	1.7	93.6 ms	1.95 ms		
70 °C	10	1.7	62.0 ms	1.89 ms		

LCC1113-D (AR Coating: 1650 - 3000 nm)						
Temperature V ₁ V ₂ Rise Time Fall Time						
25.6 °C	10	1.3	586 ms	7.0 ms		
45 °C	10	1.3	264 ms	3.0 ms		
60 °C	10	1.3	184 ms	1.5 ms		
70 °C	10	1.3	162 ms	1.0 ms		

LCC1113-MIR (AR Coating: 3600 - 5600 nm)						
Temperature V ₁ V ₂ Rise Time Fall Time						
25.6 °C	10	1.9	4400 ms	94.0 ms		
45 °C	10	1.9	2200 ms	37.5 ms		
60 °C	10	1.9	1400 ms	28.3 ms		
70 °C	10	1.9	990 ms	23.6 ms		

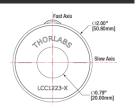
Hide Applications

APPLICATIONS

Alignment

The slow (extraordinary) axis of the liquid crystal retarder corresponds to the orientation of the long axis of the liquid crystal molecules when no voltage is being applied. Applying a voltage will cause the orientation direction of the liquid crystal molecules to rotate out of the plane of the drawing to the right, changing the retardation. Thorlabs LC retarders are nematic liquid crystal devices, which must be driven with an AC voltage in order to prevent the accumulation of ions and free charges, which degrades performance and can cause the device to burn out.

In order to precisely align the axis of the liquid crystal cell, mount the retarder in an appropriate rotation mount (e.g. the RSP1(/M) or the CRM1P(/M) for our Ø10 mm clear aperture retarders and RSP2(/M) or the LCRM2(/M) for our Ø20 mm clear aperture retarders). Then set up a detector or power meter to monitor the transmission of a beam through a pair of crossed linear polarizers. Next place the LC retarder between two crossed polarizers with the slow axis aligned with the transmission axis of the first polarizer. Then slowly rotate it until the transmitted intensity is minimized. In this configuration, the LC retarder is ready for phase modulation applications.



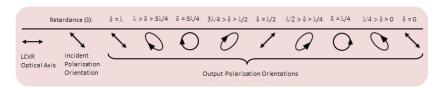
Drawing indicates the slow and fast axes

To operate as a light intensity modulator or shutter, again find the minimum transmitted intensity as prescribed above. Once the minimum is found, rotate the retarder by ±45°. This will maximize the transmitted intensity through the crossed polarizers for most LC retarders (e.g., zero-order quarter- or half-wave plates). However, this rule of thumb does not rigidly hold for multi-wave phase retarders using broadband sources due to the wavelength dependency of the retardation.

Applications

Polarization Control with a Liquid Crystal Variable Retarder

The LCVR can be effectively used as a variable zero-order wave plate over a broad spectrum of wavelengths. The optical axis of the LCVR is defined as the major axis of the liquid crystal molecules when no voltage is being applied to the cell, which are all aligned due to the LC alignment layer. When using the LCVR to control the polarization of a beam, the linearly polarized input beam should be aligned so that its polarization axis is oriented at an angle of 45° with respect to the optical axis of the LCVR in order to maximize the dynamic range of the optic. The schematic below shows how the output state of polarization will change as retardance is decreased (RMS voltage increased).



Pure Phase Retarder with Liquid Crystal Variable Retarder

In order to only effect the phase of the incident beam, the linearly polarized input beam must have its polarization axis aligned with the optical axis of the liquid crystal retarder. As V_{rms} is increased, the phase offset in the beam is decreased. Pure phase retarders are often used in interferometers to alter the optical path length of one arm of the interferometer with respect to the other. With an LCVR, this can be done actively.

Hide Damage Thresholds

DAMAGE THRESHOLDS

Damage Threshold Data for Thorlabs' Liquid Crystal Variable Retarders

The specifications to the right are measured data for Thorlabs' Liquid Crystal Variable Retarders.

Damage Threshold Specifications				
Item # Suffix Damage Threshold				
-A	Pulsed (ns)	2.04 J/cm ² (532 nm, 10 Hz, 8 ns, Ø200 μm)		
-A	Pulsed (fs)	0.024 J/cm ² (532 nm, 100 Hz, 76 fs, Ø162 μm)		
-В	Pulsed (ns)	3.56 J/cm ² (810 nm, 10 Hz, 7.6 ns, Ø234 μm)		
-6	Pulsed (fs)	0.023 J/cm ² (800 nm, 100 Hz, 36.4 fs, Ø189 μm)		
-C	Pulsed (ns)	2.5 J/cm² (1542 nm, 10 Hz, 10 ns, Ø458 μm)		
-6	Pulsed (fs)	0.161 J/cm ² (1550 nm, 100 Hz, 70 fs, Ø145 μm)		
	İ			

-D	Pulsed (ns)	0.082 J/cm ² (2000 nm, 10 Hz, 6.5 ns, Ø292 μm)
-0	Pulsed (fs)	0.05 J/cm ² (2000 nm, 100 Hz, 100 fs, Ø220 μm)

Laser Induced Damage Threshold Tutorial

The following is a general overview of how laser induced damage thresholds are measured and how the values may be utilized in determining the appropriateness of an optic for a given application. When choosing optics, it is important to understand the Laser Induced Damage Threshold (LIDT) of the optics being used. The LIDT for an optic greatly depends on the type of laser you are using. Continuous wave (CW) lasers typically cause damage from thermal effects (absorption either in the coating or in the substrate). Pulsed lasers, on the other hand, often strip electrons from the lattice structure of an optic before causing thermal damage. Note that the guideline presented here assumes room temperature operation and optics in new condition (i.e., within scratch-dig spec, surface free of contamination, etc.). Because dust or other particles on the surface of an optic can cause damage at lower thresholds, we recommend keeping surfaces clean and free of debris. For more information on cleaning optics, please see our Optics Cleaning tutorial.

Testing Method

Thorlabs' LIDT testing is done in compliance with ISO/DIS 11254 and ISO 21254 specifications.

First, a low-power/energy beam is directed to the optic under test. The optic is exposed in 10 locations to this laser beam for 30 seconds (CW) or for a number of pulses (pulse repetition frequency specified). After exposure, the optic is examined by a microscope (~100X magnification) for any visible damage. The number of locations that are damaged at a particular power/energy level is recorded. Next, the power/energy is either increased or decreased and the optic is exposed at 10 new locations. This process is repeated until damage is observed. The damage threshold is then assigned to be the highest power/energy that the optic can withstand without causing damage. A histogram such as that below represents the testing of one BB1-E02 mirror.



The photograph above is a protected aluminumcoated mirror after LIDT testing. In this particular test, it handled 0.43 J/cm² (1064 nm, 10 ns pulse, 10 Hz, Ø1.000 mm) before damage.

According to the test, the damage threshold of the mirror was 2.00 J/cm² (532 nm, 10 ns pulse, 10 Hz, Ø0.803 mm). Please keep in mind that these tests are performed on clean optics, as dirt and contamination can significantly lower the damage threshold of a component. While the test results are only representative of one coating run, Thorlabs specifies damage threshold values that account for coating variances.

Continuous Wave and Long-Pulse Lasers

When an optic is damaged by a continuous wave (CW) laser, it is usually due to the melting of the surface as a result of absorbing the laser's energy or damage

the melting of the surface as a result of absorbing the laser's energy or damage to the optical coating (antireflection) [1]. Pulsed lasers with pulse lengths longer than 1 µs can be treated as CW lasers for LIDT discussions.

Example Exposure Histogram

No Damage
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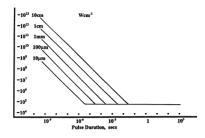
Example Test Data						
Fluence	# of Tested Locations	Locations with Damage	Locations Without Damage			
1.50 J/cm ²	10	0	10			
1.75 J/cm ²	10	0	10			
2.00 J/cm ²	10	0	10			
2.25 J/cm ²	10	1	9			
3.00 J/cm ²	10	1	9			
5.00 J/cm ²	10	9	1			

When pulse lengths are between 1 ns and 1 μ s, laser-induced damage can occur either because of absorption or a dielectric breakdown (therefore, a user must check both CW and pulsed LIDT). Absorption is either due to an intrinsic property of the optic or due to surface irregularities; thus LIDT values are only valid for optics meeting or exceeding the surface quality specifications given by a manufacturer. While many optics can handle high power CW lasers, cemented (e.g., achromatic doublets) or highly absorptive (e.g., ND filters) optics tend to have lower CW damage thresholds. These lower thresholds are due to absorption or scattering in the cement or metal coating.

Pulsed lasers with high pulse repetition frequencies (PRF) may behave similarly to CW beams. Unfortunately, this is highly dependent on factors such as absorption and thermal diffusivity, so there is no reliable method for determining when a high PRF laser will damage an optic due to thermal effects. For beams with a high PRF both the average and peak powers must be compared to the equivalent CW power. Additionally, for highly transparent materials, there is little to no drop in the LIDT with increasing PRF.

In order to use the specified CW damage threshold of an optic, it is necessary to know the following:

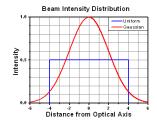
- Wavelength of your laser
- 2. Beam diameter of your beam (1/e²
- 3. Approximate intensity profile of your beam (e.g., Gaussian)
- Linear power density of your beam (total power divided by 1/e² beam diameter)



LIDT in linear power density vs. pulse length and spot size. For long pulses to CW, linear power density becomes a constant with spot size. This graph was obtained from [1].

Thorlabs expresses LIDT for CW lasers as a linear power density measured in W/cm. In this regime, the LIDT given as a linear power density can be applied to any beam diameter; one does not need to compute an adjusted LIDT to adjust for changes in spot size, as demonstrated by the graph to the right. Average linear power density can be calculated using the equation below.

$$Linear\ Power\ Density = \frac{Power}{Beam\ Diameter}$$



The calculation above assumes a uniform beam intensity profile. You must now consider hotspots in the beam or other non-uniform intensity profiles and roughly calculate a maximum

power density. For reference, a Gaussian beam typically has a maximum power density that is twice that of the uniform beam (see lower right).

Now compare the maximum power density to that which is specified as the LIDT for the optic. If the optic was tested at a wavelength other than your operating wavelength, the damage threshold must be scaled appropriately. A good rule of thumb is that the damage threshold has a linear relationship with wavelength such that as you move to shorter wavelendths, the damage threshold decreases (i.e., a LIDT of 10 W/cm at 1310 nm scales to 5 W/cm at 655 nm):

$Adjusted\ LIDT = LIDT\ Power\left(\frac{Your\ Wavelength}{LIDT\ Wavelength}\right)$

While this rule of thumb provides a general trend, it is not a quantitative analysis of LIDT vs wavelength. In CW applications, for instance, damage scales more strongly with absorption in the coating and substrate, which does not necessarily scale well with wavelength. While the above procedure provides a good rule of thumb for LIDT values, please contact Tech Support if your wavelength is different from the specified LIDT wavelength. If your power density is less than the adjusted LIDT of the optic, then the optic should work for your application.

Please note that we have a buffer built in between the specified damage thresholds online and the tests which we have done, which accommodates variation between batches. Upon request, we can provide individual test information and a testing certificate. The damage analysis will be carried out on a similar optic (customer's optic will not be damaged). Testing may result in additional costs or lead times. Contact Tech Support for more information.

Pulsed Lasers

As previously stated, pulsed lasers typically induce a different type of damage to the optic than CW lasers. Pulsed lasers often do not heat the optic enough to damage it; instead, pulsed lasers produce strong electric fields capable of inducing dielectric breakdown in the material. Unfortunately, it can be very difficult to compare the LIDT specification of an optic to your laser. There are multiple regimes in which a pulsed laser can damage an optic and this is based on the laser's pulse length. The highlighted columns in the table below outline the relevant pulse lengths for our specified LIDT values.

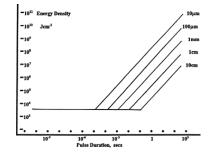
Pulses shorter than 10⁻⁹ s cannot be compared to our specified LIDT values with much reliability. In this ultra-short-pulse regime various mechanics, such as multiphoton-avalanche ionization, take over as the predominate damage mechanism [2]. In contrast, pulses between 10⁻⁷ s and 10⁻⁴ s may cause damage to an optic either because of dielectric breakdown or thermal effects. This means that both CW and pulsed damage thresholds must be compared to the laser beam to determine whether the optic is suitable for your application.

Pulse Duration	t < 10 ⁻⁹ s	10 ⁻⁹ < t < 10 ⁻⁷ s	10 ⁻⁷ < t < 10 ⁻⁴ s	t > 10 ⁻⁴ s
Damage Mechanism	Avalanche lonization	Dielectric Breakdown	Dielectric Breakdown or Thermal	Thermal
Relevant Damage Specification	No Comparison (See Above)	Pulsed	Pulsed and CW	CW

When comparing an LIDT specified for a pulsed laser to your laser, it is essential to know the following:

- 1. Wavelength of your laser
- 2. Energy density of your beam (total energy divided by 1/e² area)
- 3. Pulse length of your laser
- 4. Pulse repetition frequency (prf) of your laser
- 5. Beam diameter of your laser (1/e 2)
- 6. Approximate intensity profile of your beam (e.g., Gaussian)

The energy density of your beam should be calculated in terms of J/cm^2 . The graph to the right shows why expressing the LIDT as an energy density provides the best metric for short pulse sources. In this regime, the LIDT given as an energy density can be applied to any beam diameter; one does not need to compute an adjusted LIDT to adjust for changes in spot size. This calculation assumes a uniform beam intensity profile. You must now adjust this energy density to account for hotspots or other nonuniform intensity profiles and roughly calculate a maximum energy density. For reference a Gaussian beam typically has a maximum energy density that is twice that of the $1/e^2$ beam.



LIDT in energy density vs. pulse length and spot size. For short pulses, energy density becomes a constant with spot size. This graph was obtained from [1].

Now compare the maximum energy density to that which is specified as the LIDT for the optic. If the optic was tested at a wavelength other than your operating

wavelength, the damage threshold must be scaled appropriately [3]. A good rule of thumb is that the damage threshold has an inverse square root relationship with wavelength such that as you move to shorter wavelengths, the damage threshold decreases (i.e., a LIDT of 1 J/cm² at 1064 nm scales to 0.7 J/cm² at 532 nm):

$$Adjusted\ LIDT = LIDT\ Energy \sqrt{\frac{Your\ Wavelength}{LIDT\ Wavelength}}$$

You now have a wavelength-adjusted energy density, which you will use in the following step.

Beam diameter is also important to know when comparing damage thresholds. While the LIDT, when expressed in units of J/cm², scales independently of spot size; large beam sizes are more likely to illuminate a larger number of defects which can lead to greater variances in the LIDT [4]. For data presented here, a

<1 mm beam size was used to measure the LIDT. For beams sizes greater than 5 mm, the LIDT (J/cm2) will not scale independently of beam diameter due to the larger size beam exposing more defects.

The pulse length must now be compensated for. The longer the pulse duration, the more energy the optic can handle. For pulse widths between 1 - 100 ns, an approximation is as follows:

$$Adjusted\ LIDT = LIDT\ Energy \sqrt{\frac{Your\ Pulse\ Length}{LIDT\ Pulse\ Length}}$$

Use this formula to calculate the Adjusted LIDT for an optic based on your pulse length. If your maximum energy density is less than this adjusted LIDT maximum energy density, then the optic should be suitable for your application. Keep in mind that this calculation is only used for pulses between 10⁻⁹ s and 10⁻⁷ s. For pulses between 10⁻⁷ s and 10⁻⁴ s, the CW LIDT must also be checked before deeming the optic appropriate for your application.

Please note that we have a buffer built in between the specified damage thresholds online and the tests which we have done, which accommodates variation between batches. Upon request, we can provide individual test information and a testing certificate. Contact Tech Support for more information.

- [1] R. M. Wood, Optics and Laser Tech. 29, 517 (1998).
- [2] Roger M. Wood, Laser-Induced Damage of Optical Materials (Institute of Physics Publishing, Philadelphia, PA, 2003).
- [3] C. W. Carr et al., Phys. Rev. Lett. 91, 127402 (2003).
- [4] N. Bloembergen, Appl. Opt. 12, 661 (1973).

Hide LIDT Calculations

LIDT CALCULATIONS

In order to illustrate the process of determining whether a given laser system will damage an optic, a number of example calculations of laser induced damage threshold are given below. For assistance with performing similar calculations, we provide a spreadsheet calculator that can be downloaded by clicking the button to the right. To use the calculator, enter the specified LIDT value of the optic under consideration and the relevant parameters of your

LIDT Calculator

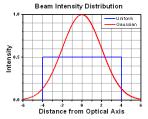
laser system in the green boxes. The spreadsheet will then calculate a linear power density for CW and pulsed systems, as well as an energy density value for pulsed systems. These values are used to calculate adjusted, scaled LIDT values for the optics based on accepted scaling laws. This calculator assumes a Gaussian beam profile, so a correction factor must be introduced for other beam shapes (uniform, etc.). The LIDT scaling laws are determined from empirical relationships; their accuracy is not guaranteed. Remember that absorption by optics or coatings can significantly reduce LIDT in some spectral regions. These LIDT values are not valid for ultrashort pulses less than one nanosecond in duration.

CW Laser Example

Suppose that a CW laser system at 1319 nm produces a 0.5 W Gaussian beam that has a $1/e^2$ diameter of 10 mm. A naive calculation of the average linear power density of this beam would yield a value of 0.5 W/cm, given by the total power divided by the beam diameter:

$$Linear\ Power\ Density = \frac{Power}{Beam\ Diameter}$$

However, the maximum power density of a Gaussian beam is about twice the maximum power density of a uniform beam, as shown in the graph to the right. Therefore, a more accurate determination of the maximum linear power density of the system is 1 W/cm.



A Gaussian beam profile has about twice the maximum intensity of a uniform beam profile.

An AC127-030-C achromatic doublet lens has a specified CW LIDT of 350 W/cm, as tested at 1550 nm. CW damage threshold values typically scale directly with the wavelength of the laser source, so this yields an adjusted LIDT value:

$$Adjusted\ LIDT = LIDT\ Power\left(\frac{Your\ Wavelength}{LIDT\ Wavelength}\right)$$

The adjusted LIDT value of 350 W/cm x (1319 nm / 1550 nm) = 298 W/cm is significantly higher than the calculated maximum linear power density of the laser system, so it would be safe to use this doublet lens for this application.

Pulsed Nanosecond Laser Example: Scaling for Different Pulse Durations

Suppose that a pulsed Nd:YAG laser system is frequency tripled to produce a 10 Hz output, consisting of 2 ns output pulses at 355 nm, each with 1 J of energy, in a Gaussian beam with a 1.9 cm beam diameter (1/e²). The average energy density of each pulse is found by dividing the pulse energy by the beam area:

$$Energy\ Density = \frac{Pulse\ Energy}{Beam\ Area}$$

As described above, the maximum energy density of a Gaussian beam is about twice the average energy density. So, the maximum energy density of this beam is ~0.7 J/cm².

The energy density of the beam can be compared to the LIDT values of 1 J/cm² and 3.5 J/cm² for a BB1-E01 broadband dielectric mirror and an NB1-K08 Nd:YAG laser line mirror, respectively. Both of these LIDT values, while measured at 355 nm, were determined with a 10 ns pulsed laser at 10 Hz. Therefore, an adjustment must be applied for the shorter pulse duration of the system under consideration. As described on the previous tab, LIDT values in the nanosecond pulse regime scale with the square root of the laser pulse duration:

Adjusted LIDT = LIDT Energy $\sqrt{\frac{Your\ Pulse\ Length}{LIDT\ Pulse\ Length}}$

This adjustment factor results in LIDT values of 0.45 J/cm² for the BB1-E01 broadband mirror and 1.6 J/cm² for the Nd:YAG laser line mirror, which are to be compared with the 0.7 J/cm² maximum energy density of the beam. While the broadband mirror would likely be damaged by the laser, the more specialized laser line mirror is appropriate for use with this system.

Pulsed Nanosecond Laser Example: Scaling for Different Wavelengths

Suppose that a pulsed laser system emits 10 ns pulses at 2.5 Hz, each with 100 mJ of energy at 1064 nm in a 16 mm diameter beam (1/e2) that must be attenuated with a neutral density filter. For a Gaussian output, these specifications result in a maximum energy density of 0.1 J/cm². The damage threshold of an NDUV10A Ø25 mm, OD 1.0, reflective neutral density filter is 0.05 J/cm² for 10 ns pulses at 355 nm, while the damage threshold of the similar NE10A absorptive filter is 10 J/cm² for 10 ns pulses at 532 nm. As described on the previous tab, the LIDT value of an optic scales with the square root of the wavelength in the nanosecond pulse regime:

$$Adjusted\ LIDT = LIDT\ Energy \sqrt{\frac{Your\ Wavelength}{LIDT\ Wavelength}}$$

This scaling gives adjusted LIDT values of 0.08 J/cm² for the reflective filter and 14 J/cm² for the absorptive filter. In this case, the absorptive filter is the best choice in order to avoid optical damage.

Pulsed Microsecond Laser Example

Consider a laser system that produces 1 µs pulses, each containing 150 µJ of energy at a repetition rate of 50 kHz, resulting in a relatively high duty cycle of 5%. This system falls somewhere between the regimes of CW and pulsed laser induced damage, and could potentially damage an optic by mechanisms associated with either regime. As a result, both CW and pulsed LIDT values must be compared to the properties of the laser system to ensure safe operation.

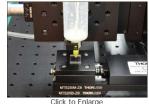
If this relatively long-pulse laser emits a Gaussian 12.7 mm diameter beam (1/e²) at 980 nm, then the resulting output has a linear power density of 5.9 W/cm and an energy density of 1.2 x 10⁻⁴ J/cm² per pulse. This can be compared to the LIDT values for a WPQ10E-980 polymer zero-order quarter-wave plate, which are 5 W/cm for CW radiation at 810 nm and 5 J/cm² for a 10 ns pulse at 810 nm. As before, the CW LIDT of the optic scales linearly with the laser wavelength, resulting in an adjusted CW value of 6 W/cm at 980 nm. On the other hand, the pulsed LIDT scales with the square root of the laser wavelength and the square root of the pulse duration, resulting in an adjusted value of 55 J/cm² for a 1 µs pulse at 980 nm. The pulsed LIDT of the optic is significantly greater than the energy density of the laser pulse, so individual pulses will not damage the wave plate. However, the large average linear power density of the laser system may cause thermal damage to the optic, much like a high-power CW beam.

Hide Custom Capabilities

CUSTOM CAPABILITIES

Thorlabs' Custom Liquid Crystal Capabilities

Thorlabs offers a large variety of liquid crystal retarders from stock, including 1/2-, 3/4-, and full-wave models with a Ø10 mm or Ø20 mm clear aperture as well as 1/2-wave temperature-controlled models. However, we also offer OEM and custom retarders. The retardance range, coating, rubbing angle, temperature stabilization, and size can be customized to meet many unique optical designs. We also offer other custom liquid crystal devices, such as empty LC cells, polarization rotators, and noise eaters. For more information about ordering a custom liquid crystal device, please contact Thorlabs' technical support.



Liquid Crystal Cell Seal Application

Our engineers work directly with our customers to discuss the specifications and other design aspects of a custom liquid crystal retarder. They will analyze both the design and feasibility to ensure the custom products are manufactured to high-quality standards and in a timely manner.

Polyimide (PI) Coating and Rubbing - Custom Alignment Angle

In their nematic phase, liquid crystal molecules naturally align to an average orientation, which together with their stretched shape, creates an optical anisotropy, or direction-dependent optical effect. The orientation of the LC molecules in an LC cell, in the absence of an applied voltage, is determined by the alignment layer, created by the polyimide (PI) coating and rubbing angle. Rubbing creates grooves, which the liquid crystal molecules will align to. Users can choose any initial orientation of LC molecules by specifying the rubbing angle.



Click to Enlarge Custom Liquid Crystal Cell Without Case

Custom Cell Spacing

The wall spacing inside of the liquid crystal cell, which determines the thickness of LC material, can be customized during the manufacturing process. The retardance range of an LC cell is dependent on the LC material thickness:

$$\delta = \frac{2\pi d\Delta n}{\lambda_{\nu}}$$

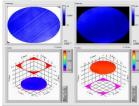
Here, δ is the retardance in waves, d is the thickness of the LC material, λ_{ν} is the wavelength of light, and Δn is the birefringence of the LC material used. Thus, for a given wavelength, the retardance is determined by the wall spacing inside the LC cell (i.e., the thickness of LC layer). We offer standard retardance ranges of $\lambda/2$ to 30 nm, $3\lambda/4$ to 30 nm, and λ to 30 nm, but higher retardance ranges may also be ordered.

Custom Liquid Crystal Material

Customers can also provide their own liquid crystal material, and



Click to Enlarge
Liquid Crystal Cell Filling in a Vacuum Chamber





Thorlabs will use it to fill the liquid crystal cell. Since different liquid crystal materials have different birefringence values, varying the material enables a different retardance range.

Custom Liquid Crystal Cell Test Result

Setup

Temperature Control/Switching Time

Custom Liquid Crystal Cell Test A temperature sensor can also be integrated into the LC variable retarder. Using a temperature controller, the temperature of the retarder can be actively stabilized to within ±0.1 °C. The viscosity of the liquid crystal material is

lowered at higher temperatures, allowing the retarder to switch from one state to another due to the decreased viscosity. An active temperature control system can be used to heat the retarder, allowing it to operate at higher switching speeds.

Assembly / Housing

If desired, we can manufacture custom liquid crystal retarders without housings.

Each LC retarder is tested for birefringence, uniformity, and fast axis angle, using the measurement setup shown in the photo to the left. The equipment measures the 2-dimensional birefringence distribution using wave plates and a CCD camera. The image to the right shows a sample test result of a liquid crystal retarder, showing excellent uniformity.

For More Information

Contact Thorlabs' technical support for more information about our custom liquid crystal device options or to place an order.

Hide Patterned Retarders

PATTERNED RETARDERS

Features

- Build a Custom Microretarder
- · Customize Size, Shape, and Substrate Material
- · Retardance Range: 50 550 nm
- Fast Axis Resolution: <1°
- · Retardance Fluctuations Under 30 nm

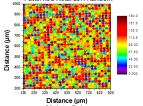
Applications

- 3D Displays
- Polarization Imaging
- · Diffractive Optical Applications: Polarization Gratings, Polarimetry, and Beam Steering

Custom Capability	Custom Specification
Patterned Retarder Size	Ø100 µm to Ø2"
Patterned Retarder Shape	Any
Microretarder Size	≥Ø30 µm
Microretarder Shape	Round or Square
Retardance Range @ 632.8 nm	50 to 550 nm
Substrate	N-BK7, UV Fused Silica, or Other Glass
Substrate Size	Ø5 mm to Ø2"
AR Coating	-A: 350 - 700 nm -B: 650 - 1050 nm -C: 1050 - 1700 nm

Thorlabs offers customizable patterned retarders, available in any pattern size from Ø100 μm to Ø2" and any substrate size from Ø5 mm to Ø2". These custom retarders are composed of an array of microretarders, each of which has a fast axis aligned to a different angle than its neighbor. The size and shape of the microretarders are also customizable. They can be as small as 30 μm and in shapes including circles and squares. This control over size and shape of the individual microretarders allows us to construct a large array of various patterned retarders to meet nearly any experimental or device need.

These patterned retarders are constructed from our liquid crystals and liquid crystal polymers. Using photo alignment technology, we can secure the fast axis of each microretarder to any angle within a resolution of <1°. Figures 1 - 3 show examples of our patterned retarders. The figures represent measured results of the patterned retarder captured on an imaging polarimeter and demonstrate that the fast axis orientation of any one individual microretarder can be controlled deterministically and separately from its neighbors.

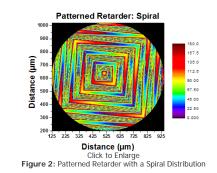


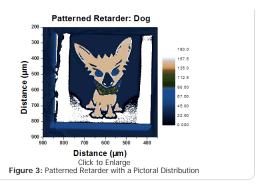
Click to Enlarge
Figure 1: Patterned Retarder with Random Distribution

The manufacturing process for our patterned retarders is controlled completely in house. It begins by preparing the substrate, which is typically N-BK7 or UV fused silica (although other glass substrates may be compatible as well). The substrate is then coated with a layer of photoalignment material and placed in our patterned retarder system where sections are exposed to linearly polarized light to set the fast axis of a microretarder. The area of the exposed sections depends on the desired size of the microretarder; the fast axis can be set between 0° and 180° with a resolution <1°. Once set, the liquid crystal cell is constructed by coating the device with a liquid crystal polymer and curing it with UV light.

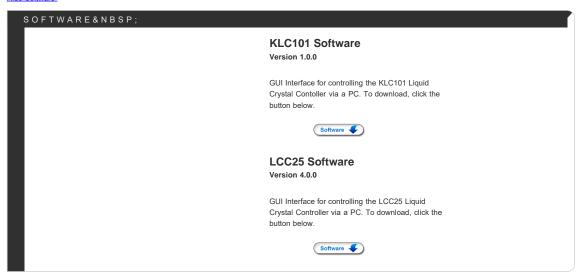
Thorlabs' LCP depolarizers provide one example of these patterned retarders. In principle, a truly randomized pattern may be used as a depolarizer, since it scrambles the input polarization spatially. However, such a pattern will also introduce a large amount of diffraction. For our depolarizers, we designed a linearly ramping fast axis angle and retardance that can depolarize both broadband and monochromatic beams down to diameters of 0.5 mm without introducing additional diffraction. For more details, see the webpage for our LCP depolarizers.

By supplying Thorlabs with a drawing of the desired patterned retarder or an excel file of the fast axis distribution, we can construct almost any patterned retarder. For more information on creating a patterned retarder, please contact Tech Support.





Hide Software



Hide Ø10 mm Compensated Full-Wave Liquid Crystal Retarders, Mounted

Ø10 mm Compensated Full-Wave Liquid Crystal Retarders, Mounted



- Ø10 mm Clear Aperture
- Retardance: 0 nm to λ
- Compensated for 0 nm Minimum Retardance
- 1" Outer Diameter
- 3 Standard AR Coatings Available

Thorlabs' Ø10 mm clear aperture, compensated, full-wave liquid crystal retarders are available with AR coatings for 350 - 700 nm (LCC1413-A), 650 - 1050 nm (LCC1413-B), or 1050 - 1700 nm (LCC1413-C). The compensator integrated within the retarder enables 0 nm minimal retardation at a specific driving voltage between 5 V and 20 V (please refer to the *Performance* tab for more information). These retarders have an outer diameter of 1", making them compatible with any of our Ø1" optic mounts for 9 mm thick optics. The slow axis is indicated by an engraved line on the front of the

Key Specifications ^a					
Item #	LCC1413-A LCC1413-B LCC1413-C				
Wavelength Range	350 - 700 nm	650 - 1050 nm	1050 - 1700 nm		
Retardance Range		0 nm to >λ			
Switching Time	39.4 ms / 310 μs				
(Rise/Fall, Typical)	@ 25.6 °C	@ 25.6 °C	@ 25.6 °C		
Retardance	<\\/20				
Uniformity (RMS)b					
Surface Quality		60-40 Scratch-Dig			

- a. See the Specs tab for complete specifications.
- b. Specified Over the Entire Clear Aperture

retarder. Additionally, these mounted retarders have a BNC cable which is approximately 930 mm long for the electrical connection. As this retarder has a 1" outer diameter, it can be mounted using the RSP1(/M) Post-Mountable Rotation Mount or the CRM1PT(/M) 30 mm Cage Rotation Mount.

Part Number	Description	Price	Availability
LCC1413-A	Compensated Full-Wave LC Retarder, Ø10 mm CA, ARC: 350 - 700 nm	\$989.94	Today
LCC1413-B	Compensated Full-Wave LC Retarder, Ø10 mm CA, ARC: 650 - 1050 nm	\$989.94	7-10 Days
LCC1413-C	Compensated Full-Wave LC Retarder, Ø10 mm CA, ARC: 1050 - 1700 nm	\$989.94	Today

Hide Ø10 mm Uncompensated Full-Wave Liquid Crystal Retarders, Mounted

Ø10 mm Uncompensated Full-Wave Liquid Crystal Retarders, Mounted



- Ø10 mm Clear Aperture
- ▶ Retardance: ~30 nm to λ
- ▶ 1" Outer Diameter
- 5 Standard AR Coatings Available

Thorlabs' Ø10 mm clear aperture, uncompensated, full-wave liquid crystal retarders are available with AR coatings for 350 - 700 nm (LCC1113-A), 650 - 1050 nm (LCC1113-B), 1050 - 1700 nm (LCC1113-C), 1650 - 3000 nm (LCC1113-D), or 3600 - 5600 nm (LCC1113-MIR) light. These retarders have an outer diameter of 1", making them compatible with any of our Ø1" optic mounts for 8 mm thick optics. The slow axis is indicated by an engraved line on the front of the retarder. Additionally, these mounted retarders have a 930 mm long BNC cable for the electrical connection. As this retarder has a 1" outer diameter, it can be mounted using the RSP1(/M) Post-Mountable Rotation Mount or the CRM1PT(/M) 30 mm Cage Rotation Mount.

Key Specifications ^a						
Item #	LCC1113-A LCC1113-B LCC1113-C LCC1113-D LCC1113-MIR					
Wavelength Range	350 - 700 nm	650 - 1050 nm	1050 - 1700 nm	1650 - 3000 nm	3600 - 5600 nm	
Retardance Range			~30 nm to >λ			
Switching Time (Rise/Fall, Typical)	59.6 ms / 1.45 ms @ 22 °C	124 ms / 2.94 ms @ 25.6 °C	236 ms / 4.89 ms @ 25.6 °C	ms 586 ms / 7.0 ms 4.4 s / 94 ms @ 25.6 °C @ 25.6 °C		
Retardance Uniformity (RMS) ^b	<\table 50 <\table 10			110		
Surface Quality	40-20 Scratch-Dig 60-40 Scratch-Dig			ratch-Dig		

- a. See the Specs tab for complete specifications.
- b. Specified Over the Entire Clear Aperture

Part Number	Description	Price	Availability
LCC1113-A	Uncompensated Full-Wave LC Retarder, Ø10 mm CA, ARC: 350 - 700 nm	\$772.16	Today
LCC1113-B	Uncompensated Full-Wave LC Retarder, Ø10 mm CA, ARC: 650 - 1050 nm	\$772.16	Today
LCC1113-C	Uncompensated Full-Wave LC Retarder, Ø10 mm CA, ARC: 1050 - 1700 nm	\$772.16	Today
LCC1113-D	Uncompensated Full-Wave LC Retarder, Ø10 mm CA, ARC: 1650 - 3000 nm	\$1,152.97	Today
LCC1113-MIR	Uncompensated Full-Wave LC Retarder, Ø10 mm CA, ARC: 3600 - 5600 nm	\$2,977.95	Today

Hide Ø20 mm Compensated Full-Wave Liquid Crystal Retarders, Mounted

Ø20 mm Compensated Full-Wave Liquid Crystal Retarders, Mounted



- Ø20 mm Large Clear Aperture
- Retardance: 0 nm to λ
- Compensated for 0 nm Minimal Retardation
- SM2 External Threading
- 3 Standard AR Coatings Available

Thorlabs' Ø20 mm clear aperture, compensated, full-wave liquid crystal retarders are available with AR coatings for 350 - 700 nm (LCC1423-A), 650 - 1050 nm (LCC1423-B), or 1050 - 1700 nm (LCC1423-C). The compensator integrated within the retarder enables 0 nm minimal retardation at a specific driving voltage between 5 V and 20 V (please refer to the *Performance* tab for more information). These retarders have external SM2 threading, making them compatible with any of our Ø2" threaded optic mounts. This includes mounts like the RSP2, an SM2 mount which provides precise

Key Specifications ^a					
Item #	LCC1423-A LCC1423-B LCC1423-C				
Wavelength Range	350 - 700 nm	650 - 1050 nm	1050 - 1700 nm		
Retardance Range	0 nm to >λ				
Switching Time (Rise/Fall, Typical)	39.4 ms / 310 μs @ 25.6 °C	129 ms / 660 μs @ 25.6 °C	209 ms / 193 μs @ 25.6 °C		
Retardance Uniformity (RMS) ^b	<n20< th=""></n20<>				
Surface Quality	60-40 Scratch-Dig				

- a. See the Specs tab for complete specifications.
- b. Specified Over the Entire Clear Aperture

rotational adjustment and post mounting capability, or the LCRM2 which adds 60 mm cage-mounting versatility. The slow axis is indicated by an engraved line on the front of the retarder. Additionally, these mounted retarders have a BNC cable which is approximately 944 mm long for the electrical connection.

Part Number	Description	Price	Availability
LCC1423-A	Customer Inspired! Compensated Full-Wave LC Retarder, Ø20 mm CA, ARC: 350 - 700 nm	\$1,370.16	Today
LCC1423-B	Customer Inspired! Compensated Full-Wave LC Retarder, Ø20 mm CA, ARC: 650 - 1050 nm	\$1,370.16	Today
LCC1423-C	Customer Inspired! Compensated Full-Wave LC Retarder, Ø20 mm CA, ARC: 1050 - 1700 nm	\$1,370.16	Today

Hide Ø20 mm Uncompensated Full-Wave Liquid Crystal Retarders, Mounted

Ø20 mm Uncompensated Full-Wave Liquid Crystal Retarders, Mounted



- Ø20 mm Large Clear Aperture
- ▶ Retardance: ~30 nm to λ
- 2" Outer Diameter
- 3 Standard AR Coatings Available

Thorlabs' Ø20 mm clear aperture,

uncompensated, full-wave liquid crystal retarders are available with AR coatings for 350 - 700 nm (LCC1223-A), 650 - 1050 nm (LCC1223-B), or 1050 - 1700 nm (LCC1223-C) light. These retarders have an outer diameter of 2", making them compatible with any of our Ø2" optic mounts for 13 mm thick optics. As this retarder has a 2" outer diameter, it can be mounted using the RSP2 Post-Mountable Rotation Mount or the LCRM2 60 mm Cage Plate Rotation Mount. Additionally, these mounted retarders have a 930 mm long BNC cable for the electrical connection.

Key Specifications ^a						
Item #	LCC1223-A LCC1223-B LCC1223-C					
Wavelength Range	350 - 700 nm	650 - 1050 nm	1050 - 1700 nm			
Retardance Range	~30 nm to >λ					
Switching Time (Rise/Fall, Typical)	59.6 ms / 1.45 ms @ 22 °C	124 ms / 2.94 ms @ 25.6 °C	236 ms / 4.89 ms @ 25.6 °C			
Retardance Uniformity (RMS) ^b	<n50< th=""></n50<>					
Surface Quality	40-20 Scratch-Dig					

- a. See the Specs tab for complete specifications.
- b. Specified Over the Entire Clear Aperture

Part Number	Description	Price	Availability
LCC1223-A	Uncompensated Full-Wave LC Retarder, Ø20 mm CA, ARC: 350 - 700 nm	\$1,034.19	Today
LCC1223-B	Uncompensated Full-Wave LC Retarder, Ø20 mm CA, ARC: 650 - 1050 nm	\$1,034.19	Today
LCC1223-C	Uncompensated Full-Wave LC Retarder, Ø20 mm CA, ARC: 1050 - 1700 nm	\$1,034.19	Today

Hide Liquid Crystal Controllers

Liquid Crystal Controllers



- ▶ 0 to ±25 VAC Square-Wave Output Voltage
 - LCC25: 2000 ± 5 Hz
 - KLC101: Adjustable from 500 Hz to 10 kHz
- Internal and External Options for Modulating the Output Square-Wave Amplitude
- Edit Settings Using Device Panel or Control via USB Input from a PC

The LCC25 and KLC101 Liquid Crystal (LC) Controllers are both designed to operate Thorlabs' liquid crystal cells, rotators, and retarders (except for the LCC2415-VIS, which has an integrated controller). Each controller supplies a a square-wave AC voltage output with an amplitude that can be adjusted from 0 V_{rms} to ±25 V_{rms}. Both will automatically detect and correct any DC offset to within ±10 mV in real time, helpful for increasing the life of liquid crystal devices.

LCC25 Benchtop Controller

The LCC25 LC Controller produces a 2000 Hz square-wave AC voltage output. Both the front panel and USB interface allow the user to select two voltage levels, Voltage 1 and Voltage 2. When the LCC25 is operated in the constant voltage mode, the output of the controller will have an amplitude equal to either of the two set voltage levels. In Modulation mode, the amplitude of the 2,000 Hz square-wave output will switch between Voltage 1 and Voltage 2 at a frequency that can be set internally (0.5 Hz to 150 Hz) or triggered externally with a 0 to 5 V TTL input (0.5 Hz to 500 Hz). The LCC25 controller's software package also allows the user to define a voltage sequence by specifying a starting voltage, ending voltage, step size, and dwell time.

Please visit the LCC25 controller page for more information on this controller's features.

KLC101 K-Cube™ Controller

The KLC101 K-Cube™ LC Controller is a part of Thorlabs' growing line of high-end, compact controllers. It supplies a square-wave AC output voltage with a user-set frequency from 500 Hz to 10 kHz and features a small 60.0 mm x 60.0 mm footprint. The controller can store two voltage and frequency combinations, PresetV1 and Preset V2, either of which can be selected as the output. Additionally, the controller top panel, software, and trigger ports allow the use to create customized output voltage sequences. The voltage can be set to switch between PresetV1 and PresetV2 at rates from 0.1 and 150 Hz using the top panel controls or software. When in this mode, one of the trigger ports will output a 5 V logic signal, with the high level indicating that the output is PresetV1 and PresetV2 and the low level indicating the output is PresetV1. Alternatively, a trigger port can be used as an input for a 5 V logic signal to switch between PresetV1 and PresetV2. The software also enables Sequence and Sweep Modes. The Sequence tool can be used to set a sequence of output voltage amplitudes, frequencies, and step durations. In Sweep mode, the KLC101 controller will scan between user-set start and end voltages with a user-specified step size and step duration.

Note that the KLC101 controller does not ship with a power supply. For applications requiring a single K-cube, the TPS002 power supply (sold below) can be used. We also offer USB controller hubs for use with multiple K-cubes.

See the full KLC101 controller web presentation for more information on the features of this controller and its power supply options.

Item #	Adjustable Output Voltage	Voltage Resolution	Adjustable Output Frequency ^a	Internal Modulation ^a	External Modulation	Slew Rate	DC Offset	Warm Up Time	Output Current (Max)	External Input Voltage (Max)
LCC25	0 to ±25 V RMS	1.0 mV	2,000 ± 5 Hz	0.5 to 150 Hz		- 10 V/µs ±10 m	±10 m\/	20 Minutes	15 mA	5 VDC
KLC101		1.0 1110	500 Hz to 10 kHz	0.1 to 150 Hz				± 10 IIIV	30 Millutes	50 mA

a. At 50% Duty Cycle

Part Number	Description		Availability
LCC25	Liquid Crystal Controller, 0-25 VAC, Square Wave, 50% Duty Cycle	\$1,512.85	Today
KLC101	K-Cube Liquid Crystal Controller (Power Supply Sold Separately)	\$861.00	Today
TPS002	±15 V/5 V Power Supply Unit with Mini-DIN Connectors for up to Two K- or T-Cubes	\$125.77	Today

Visit the Full-Wave Liquid Crystal Variable Retarders / Wave Plates page for pricing and availability information: https://www.thorlabs.com/newgrouppage9.cfm?objectgroup_id=6339

